

December 1986 Revised July 2001

### **DM7406**

# Hex Inverting Buffers with High Voltage Open-Collector Outputs

#### **General Description**

This device contains six independent buffers each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

## **Pull-Up Resistor Equations**

$$R_{MAX} = \frac{V_{O} (Min) - V_{OH}}{N_{1} (I_{OH}) + N_{2} (I_{IH})}$$

$$\mathsf{R}_{MIN} = \frac{\mathsf{V}_{O}\left(\mathsf{Max}\right) - \mathsf{V}_{OL}}{\mathsf{I}_{OL} - \mathsf{N}_{3}\left(\mathsf{I}_{IL}\right)}$$

Where:  $N_1$  ( $I_{OH}$ ) = total maximum output high current

for all outputs tied to pull-up resistor

 $\rm N_2~(I_{IH}) = total~maximum~input~high~current~for$ 

all inputs tied to pull-up resistor

 $\rm N_3 \ (I_{\rm IL}) = total \ maximum \ input \ low \ current \ for$ 

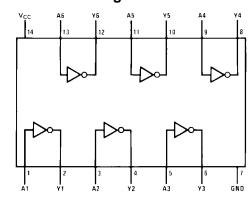
all inputs tied to pull-up resistor

#### **Ordering Code:**

Order Number	Package Number	Package Description
DM7406M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
DM7406N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

# **Connection Diagram**



#### **Function Table**

$\mathbf{Y} = \mathbf{A}$					
Input	Output				
Α	Y				
L	Н				
Н	L				

H = HIGH Logic Level L = LOW Logic Level

#### **Absolute Maximum Ratings**(Note 1)

Supply Voltage 7V Input Voltage 5.5V Output Voltage 30V Operating Free Air Temperature Range 0°C to  $+70^{\circ}$ C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

#### **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
Voн	HIGH Level Output Voltage			30	V
OL	LOW Level Output Current			40	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

 $-65^{\circ}C$  to  $+150^{\circ}C$ 

#### **Electrical Characteristics**

Storage Temperature Range

over recommended operating free air temperature range (unless otherwise noted)

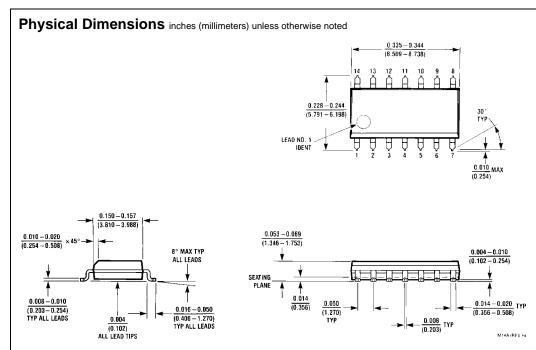
Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -12 mA			-1.5	V
I <sub>CEX</sub>	HIGH Level Output Current	$V_{CC} = Min, V_O = 30V$ $V_{IL} = Max$			250	μΑ
V <sub>OL</sub>	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$ $I_{OL} = 16 \text{ mA}, V_{CC} = Min$			0.7	V
I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$			1	mA
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.4V$			40	μА
I <sub>IL</sub>	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-1.6	mA
I <sub>CCH</sub>	Supply Current with Outputs HIGH	V <sub>CC</sub> = Max		30	48	mA
I <sub>CCL</sub>	Supply Current with Outputs LOW	V <sub>CC</sub> = Max		27	51	mA

**Note 2:** All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.

#### **Switching Characteristics**

at  $V_{CC}=5V$  and  $T_A=25^{\circ}C$ 

Symbol	Parameter	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time	C <sub>L</sub> = 15 pF		15	ns
	LOW-to-HIGH Level Output	$R_L = 110\Omega$		15	
t <sub>PHL</sub>	Propagation Delay Time			23	no
	HIGH-to-LOW Level Output			23	ns



14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow Package Number M14A

#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 0.740 - 0.770 (18.80 - 19.56)0.090 (2.286) 14 13 12 11 10 9 8 14 13 12 INDEX AREA 0.250 ± 0.010 (6.350 ± 0.254) PIN NO. 1 PIN NO. 1 IDENT 1 2 3 4 5 6 7 1 2 3 $\frac{0.092}{(2.337)}$ DIA 0.030 MAX (0.762) DEPTH OPTION 1 OPTION 02 $\frac{0.135 \pm 0.005}{(3.429 \pm 0.127)}$ 0.300 - 0.320 $\frac{0.630 - 8.128}{(7.620 - 8.128)}$ 0.060 0.145 - 0.2004° TYP Optional (1.651) (3.683 - 5.080) $\frac{0.008 - 0.016}{(0.203 - 0.406)}$ TYP 0.020 (0.508) 0.125 - 0.150 $0.075 \pm 0.015$ $\overline{(3.175 - 3.810)}$ $(1.905 \pm 0.381)$ (7.112) MIN 0.014 - 0.0230.100 ± 0.010 (2.540 ± 0.254) (0.356 - 0.584) $\frac{0.050 \pm 0.010}{(1.270 - 0.254)}$ TYP 0.325 <sup>+0.040</sup> -0.015 8.255 + 1.016

14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide Package Number N14A

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N144 (REV.F)